DSP and MIXED SIGNAL PROCESSORS

	L MODEL MODEL		MIPS	CYLCE TIME	CLK	PROGRAM		DATA	CACHE	SERIAL	HOST		TEMPERATURE			#	PRICE
MODEL						RAM	ROM			PORTS		Vcc	RANGI			Pins	100's
				nsec	MHZ		*					+3.3V	0>70	-25/85	-55/125		
FIVEI) POINT																
LIALI	Highest Perfo	owmon	aat Cana	ummant C	anal D	rooccino											
ADSP	· ·	200	50	20	25	4Kx24	5	4Kx16	DMA	2	DMA	NO	K	В		160	\$49.00
ADSP		200	66	15	25	24Kx24		16Kx16	DMA	2	DMA	NO	K	В		160	749.00 TBD
ADSP		200	60	15	30	24Kx24 24Kx24		16Kx16	DMA	2	DMA	YES	K	В		160	TBD
ADSP			00	15	30	24KX24		101010	DMA		DNIA	163		D		100	100
	General Purp	-															
ADSP	2101	40	10.24	100	10	2Kx24		1Kx16		2	NO	NO			T	68	\$194.00
ADSP	2101	50	12.5	80	12.5	2Kx24		1Kx16		2	NO	NO			T	68	\$216.00
ADSP	2101	66	16.67	60	16.67	2Kx24		1Kx16		2	NO	NO	K	В		68/80	\$17.30
ADSP	2101	80	20	50	20	2Kx24		1Kx16		2	NO	NO	K	В		68/80	\$20.75
ADSP		100	25	40	25	2Kx24		1Kx16		2	NO	NO	K	В		68/80	\$23.86
ADSP	2103	40	10.24	100	10.24	2Kx24		1Kx16		2	NO	YES	K	В		68/80	\$20.75
ADSP	2105	55	13.84	72	10.24	1/2Kx24		1Kx16		1	NO	NO	K	В		68	\$7.00
ADSP	2105	80	20	50	20	1/2Kx24		1Kx16		1	NO	NO	K	В		68	\$9.90
ADSP	2111	40	10.24	100	10	2Kx24		1Kx16		2	YES	NO			T	100	\$269.00
ADSP	2111	52	13	72	13	2Kx24		1Kx16		2	YES	NO			T	100	\$311.00
ADSP	2111	66	16.67	60	16.67	2Kx24		1Kx16		2	YES	NO	K	В		100	\$43.00
ADSP	2111	80	20	50	20	2Kx24		1Kx16		2	YES	NO	K	В		100	\$50.00
ADSP	2115	80	20	50	20	1Kx24		1/2Kx16		2	NO	NO	K	В		68/80	\$12.00
ADSP	2115	66	16.67	60	16.67	1Kx24		1/2Kx16		2	NO	NO	K	В		68/80	\$12.00
ADSP		100	25	40	40	1Kx24		1/2Kx16		2	NO	NO	K	В		68/80	\$13.80
ADSP		133	33	30	16.67	2Kx24		2Kx16		2	YES	NO	K	В		128	\$35.00
ADSP		104	26	38	13	2Kx24		2Kx16		2	YES	NO	K	В		128	\$31.50
ADSP	2173	80	20	50	10.24	2Kx24		2Kx16		2	YES	YES		В		128	\$36.23
	ROM BASED																
ADSP	2104	80	20	50	20	1/2Kx24		1/4Kx16		2	NO	NO	K			68	\$7.00
ADSP	2104	55	13.84	72	13	1/2Kx24		1/4Kx16		2	NO	YES	KL			68	\$7.00
ADSP	2109	80	20	50	20		4Kx24	1/4Kx16		1	NO	NO	K			68	CF
ADSP	2109	55	13.84	72	13		4Kx24	1/4Kx16		1	NO	YES	LK			68	CF
ADSP	2161	66	16.67	60	16.67		8Kx24	1/2Kx16		2	NO	NO	K	В		68/80	CF
ADSP	2162	40	10.24	100	10.24		8Kx24	1/2Kx16		2	NO	YES	K	В		68/80	CF
ADSP	2163	66	16.67	60	16.67		4Kx24	1/2Kx16		2	NO	NO	K	В		68/80	CF
ADSP		100	25	40	40		4Kx24	1/2Kx16		2	NO	NO	K	В		68/80	CF
ADSP	2164	40	10.24	100	10.24		4Kx24	1/2Kx16		2	NO	YES	K	В		68/80	CF
ADSP	2165	80	20	50	20	1Kx24	12KX24	4Kx16		2	YES	NO	K			68/80	CF
ADSP	2166	66	16.67	60	16.67	1Kx24	12KX24	4Kx16		2	YES	YES	K			68/80	CF
	LARGE ON CI																
	LANGE ON CI	IIIF IVIE	AVIUKI														

DSP and MIXED SIGNAL PROCESSORS

	MODEL MODEL			CYLCE CLK		PROGRAM		DATA	CACHE SERIAL		HOST		TEMPERATURE			#	PRICE
MODEL			MIPS	TIME	IN	RAM	ROM	RAM		PORTS	PORT	Vcc	RANGE			Pins	100's
				nsec	MHZ							+3.3V	0>70	-25/85	-55/125		
ADSP	2181	115	28.8	34.7	14.2	16Kx24		16Kx16		2	DMA	NO	K	В		128	\$36.00
ADSP	2181	133	33	30	16.67	16Kx24		16Kx16		2	DMA	NO	K	В		128	\$45.00
ADSP	2183	115	28.8	34.7	14.2	16Kx24		16Kx16		2	DMA	YES	K	В		128	\$51.23
ADSP	2183	133	33	30	16.67	16Kx24		16Kx16		2	DMA	YES	K	В		128	CF
ADSP	2185	133	33	30	16.67	16Kx24		16Kx16		2	DMA	NO	K	В		100	\$43.00
ADSP	2185	115	28.8	34.7	14.2	16Kx24		16Kx16		2	DMA	NO	K	В		100	\$38.70
ADSP	2185L	133	33	30	16.67	16Kx24		16Kx16		2	DMA	YES	K	LB		100	\$43.00
ADSP	2185L	115	28.8	34.7	14.2	16Kx24		16Kx16		2	DMA	YES	K	LB		100	CF
ADSP	2186L	133	33	30	16.67	8Kx24		8Kx16		2	DMA	YES	K	LB		100	\$26.19
ADSP	2186L	115	28.8	34.7	14.2	8Kx24		8Kx16		2	DMA	YES	K	LB		100	\$26.19
ADSP	2186	133	33	30	16.67	8Kx24		8Kx16		2	DMA	NO	K	В		100	Φ20.19
ADSP	2186	115	28.8	34.7	14.2	8Kx24		8Kx16		2	DMA	NO	K	В		100	\$25.20
	ING POIN		20.0	34.7	17,2	OIXAZ-F		OKATO		2	DIVIT	110	IX	В		100	Ψ23,20
1 20111		_		CYLCE	CLK	PROGRA	4M	DATA	CACHE	SERIAL	HOST		TEI	MPERA	TURE	#	PRICE
MODEL	MODEL	MODE	MFLOPS	TIME	IN	RAM	ROM	RAM		PORTS	PORT	Vcc	R	RANGE		Pins	100's
				nsec	MHZ							+3.3V	0>70	-25/85	-55/125		
ADSP	21020	133	33	30	33				32X48	2	NO	NO	K			223	\$159.00
ADSP	21020	120	30	33.3	30				32X48	0	NO	NO		В	T	223	\$176.00
ADSP	21020	100	25	40	25				32X48	0	NO	NO	K	В	Т	223	\$128.00
ADSP	21020	80	20	50	20				32X48	0	NO	NO	K	В	Т	223	\$106.00
-						Typical M	lemory Co	nfiguration		-	-						7
ADSP	21060	133	33	31	33	40Kx48		64Kx32	32X48	2	YES	NO	K	В		240	\$383.00
ADSP	21060	160	40	25	40	40Kx48		64Kx32	32X48	2	YES	NO	K	В		240	\$485.00
ADSP	21062	133	33	31	33	20Kx48		32Kx32	32X48	2	YES	NO	K	В		240	\$207.00
ADSP	21062	160	40	25	40	20Kx48		32Kx32	32X48	2	YES	NO	K	В		240	\$259.00
ADSP	21061	200	50	20	50	10Kx48		16Kx32	32X48	2	YES	NO	K	В		240	
ADSP	21061L	133	33	31	33	10Kx48		16Kx32	32X48	2	YES	YES	K	В		240	
ADSP	21061	133	33	31	33	10Kx48		16Kx32	32X48	2	YES	NO	K	В		240	\$86.00
ADSP	21061	160	40	25	40	10Kx48		16Kx32	32X48	2	YES	NO	K	В		240	\$98.00
	Quad ADSI																***************************************
AD	14060	133	640	25	40	160Kx48		256Kx32	32X48	5	YES	NO		В	Т	308	CF
MIXED	SIGNAL F	PROCES	SSORS														
AD20	MSP415	MSP415 ADSP2171 + GSM FUNCTIONS						See Data Si	heet for Sp	ecs							
	WITH ON B	OARD T	ELCOM CO	ODEC. 16	BITS												
ADSP	21MSP58	104	26	38	13	2Kx24		2Kx16		2	YES	NO	K	В		100	\$44.00
ADSP	21MSP59	104	26	38	13	2Kx24	4Kx24	2Kx16		2	YES	NO	K	В		100	CF

12/10/97

1:57 PM